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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s):           Bhattacharya et al.

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Serial No.:           09/896,059

For:                   METHOD FOR AUTOMATED DESIGN OF INTEGRATED CIRCUITS  
WITH TARGETED QUALITY OBJECTIVES USING DYNAMICALLY  
GENERATED BUILDING BLOCKS

Filed:                 June 29, 2001

Examiner:            Not Yet Assigned

Art Unit:            2825

Confirmation No.:    9475

Attorney Docket No.: 162.7107USU

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COMMISSIONER FOR PATENTS  
Washington, D.C. 20231**


**REQUEST FOR EXTENSION OF TIME**

Dear Sir:

This is a petition for extension of time for a total period of one (1) month. Based on the extension requested, the extended period for response will expire on November 21, 2001.

A check in the amount of \$55.00 (small entity) is enclosed with this Petition. The Commissioner is hereby authorized to charge any fees or credit any overpayment, to Deposit Account No. 01-0467. A duplicate copy of this Request is enclosed.

Respectfully submitted,



Date: November 20, 2001

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55.00 OP

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